

Attorney's Docket No.: 10559-487001/P11470/Intel Corporation

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hooper, et al.

Art Unit: 2664

Serial No.: 09/966,349

Examiner: Kevin D. Mew

Filed

: September 28, 2001

Assignee: Intel Corporation

Title

: MULTIPROTOCOL DECAPSULATION/ENCAPSULATION CONTROL

STRUCTURE AND PACKET PROTOCOL CONVERSION METHOD

## Mail Stop RCE

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits of the Request for Continued Examination. No fee is required.

The documents cited on the Forms PTO-1449 are in the English language, with the exception of item noted below. Item JM (JP 59111533) is in the Japanese language and is provided with an English language abstract (item KG). Hence, in

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR  $\S1.8(a)$  that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 17, 2006 Date of Deposit

Signature

Smith

Printed Typed or Name of Person Certificate

accordance with the requirements of 37 C.F.R. §1.98, as amended effective March 16, 1992, no further explanation of the listed items are necessary.

Applicant makes known to the Examiner the following U.S. applications, which are commonly owned and/or has one or more inventors in common:

U.S.S.N.	Filing Date	Docket No.
09/473,571	12/28/1999	10559-128001
09/475,614	12/30/1999	10559-137001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

Soft C. Harris Req. No. 32,030

Atty. for the Intel Corporation

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U.S. Department of Commerce Patent and Trademark Office Attorney's Docket No. 10559-487001

Application No. 09/966,349

hynformation Disclosure Statement
by Applicant
(Use several sheets if necessary)

Applicant
Donald F. Hooper, et al.

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	AA	3,373,408	3/12/1968	Ling			
	AB	3,478,322	11/11/1969	Evans			
	AC	3,736,566	5/29/1973	Anderson et al.			
	AD	3,792,441	2/12/1974	Wymore et al.			
	AE	3,940,745	2/24/1976	Sajeva			
	AF	4,016,548	4/5/1977	Law, et al.			
\	AG	4,032,899	6/28/1977	Jenny, et al.			, , , , , , , , , , , , , , , , , , , ,
•	AH	4,075,691	2/21/1978	Davis, et al.			
	AI	4,130,890	12/19/1978	Adam			
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	AK	4,514,807	4/30/1985	Nogi			
	AL	4,523,272	6/11/1985	Fukunaga et al.			
	AM	4,709,347	11/24/1987	Kirk, David L.			
1.	AN	4,745,544	5/17/1988	Renner et al.		,	
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	AP	4,831,358	5/16/1989	Ferrio, et al.			
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	AR	4,890,218	12/26/1989	Bram			
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	AT	4,991,112	2/5/1991	Callemyn			
	AU	5,115,507	5/19/1992	Callemyn			
	AV	5,140,685	8/18/1992	Sipple et al.			
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	AX	5,155,831	10/13/1992	Emma et al.			
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	Danin	Decument		nt Documents	- <u>-</u>		Fig. 5
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	CC	5,557,766	9/17/1996	Takiguchi et al.			
	CD	5,568,476	10/22/1996	Sherer, et al.			
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·	СН	5,592,622	1/7/1997	Isfeld et al.			
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	CR	5,680,641	10/21/1997	Sidman		,	
	CS	5,689,566	11/18/1997	Nguyen			
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	DA	5,742,822	4/21/1998	Motomura		· ·	
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	FH	6,085,215	7/4/2000	Ramakrishnan et al.			
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	GI	6,247,025	6/12/2001	Bacon			
	GJ	6,256,713	7/3/2001	Audityan et al.			
	GK	6,272,520	8/7/2001	Sharangpani et al.			
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	GM	6,275,505	8/14/2001	O'Loughlin et al.			
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	GY	6,347,344	2/12/2002	Baker et al.		-	
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	нт	6,526,451	2/25/2003	Kasper et al.		-			
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	HX	6,552,826	4/22/2003	Adler et al.					
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	IL	6,661,794	12/9/2003	Wolrich et al.			
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	IT	6,792,488	9/14/2004	Wolrich, et al.			
	IU	6,876,561	4/5/2005	Wolrich et al.			
	IV	6,895,457	5/17/2005	Wolrich et al.			
	IW	6,925,637	8/2/2005	Thomas et al.			
	IX	6,952,824	10/4/2005	Hooper et al.			
	IY	6,976,095	12/13/2005	Wolrich et al.			
·	IZ	6,983,350	1/3/2006	Adiletta et al.			
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	JВ	2004/0054880	3/18/2004	Bernstein et al.			
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	JF	2004/0098496	5/20/2004	Wolrich et al.			
	JG	2004/0109369	6/10/2004	Wolrich et al.			

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-487001	Application No. 09/966,349
Information Discl		Applicant Donald F. Hooper, et al.	
(Use several shed (37 CFR §1.98(b))	ets if necessary)	Filing Date September 28, 2001	Group Art Unit 2664

	Foreig	n Patent Doci	uments or P	ublished Foreign	Patent A	Application	าร	
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Tran: Yes	slation No
THICH THE	ЛН	EP 0 379 709	8/1/1990	Europe	0.000	Cubolado	103	110
	JI	EP 0 464 715	1/8/1992	Europe		-	,	
	JJ	EP 0 633 678	1/11/1995	Europe				
	JК	EP 0 745 933	12/4/1996	Europe				
	JL	EP 0 809 180	11/26/1997	Europe				
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-	JN	WO 01/16718	3/8/2001	WIPO				
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X\*= An English language abstract is provided.

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Initial	ID	Document
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-487001	Application No. 09/966,349	
	losure Statement plicant	Applicant Donald F. Hooper, et al.		
(Use several sheets if necessary) (37 CFR §1.98(b))		Filing Date September 28, 2001	Group Art Unit 2664	

	Other D	ocuments (include Author, Title, Date, and Place of Publication)
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Initial	ID	Document
	KE	Digital Semiconductor 21140A PCI Fast Ethernet LAN Controller, Hardware Reference Manual, Digital Equipment Corporation, pages i-x, 1-1 through 1-5, 2-1 through 2-12, 3-1 through 3-38, 4-31 through 5-2, 6-1 through 6-24, (March 1998)
	KF	Doyle et al. (Eds.), <i>Microsoft Press Computer Dictionary</i> , 2 <sup>nd</sup> ed., Microsoft Press: Redmond: Washington, USA, pp. 326, (1994)
	KG	English language abstract of Japanese Patent Application JP59111533, published 06/27/84, entitled: "Digital Data Arithmetic Circuit".
	КН	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, pp. 146-156, (1995)
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Other Documents (include Author, Title, Date, and Place of Publication)			
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	KX	Vibhatavanijt et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-369.	
	KY	Wazlowski et al., "PRISM-II computer and architecture," IEEE Workshop on FPGAs for Custom Computing Machines, pp. 9-16, (1993)	

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